

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: none Examiner: To be assigned

## In Re PATENT APPLICATION Of:

Applicant:	Yoshinori SHIZUNO	)
Serial No.:	10/722,446	)
Filed:	November 28, 2003	) ) STATUS REQUEST
For:	Semiconductor device with improved design freedom of external terminal	/
Attorney Ref:		) ) July 18, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please as for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

July 18, 2005

Date

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